Docket No.: SMAL.7213

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Lee et al.

GROUP: Unknown

SERIAL NO: Unknown

EXAMINER: Unknown

FILED: Herewith

FOR: CIRCUIT AND METHOD FOR CANCELLATION OF COLUMN PATTERN

NOISE IN CMOS IMAGERS

Mail Stop Patent Application Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

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INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. § 1.98(a)(2)(i), Applicant submits copies of all non-US printed patent or publications documents listed on the attached Form PTO-1449.

The Commissioner is authorized to charge Deposit Order Account No. 19-0079 for any further fee that may be required.

Respectfully submitted,

Matthew E. Connors

Registration No. 33,298

Samuels, Gauthier & Stevens, LLP 225 Franklin Street, Suite 3300

Boston, Massachusetts 02110

Telephone: 617-426-9180, Extension 112

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on October 6, 2003 shown below in an envelope, with Express Mail Label No. EL960882982US addressed to the. Mail Stop Patent Application, Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450

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FORM PTO-1449 SAMUELS, GAUTHIER & STEVENS LLP 225 Franklin Street, Boston, MA 02110

Telephone: (617) 426-9180

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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APPLICANT: Lee et al.

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| EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | | | | |